

## CLAIMS:

1. A method of controlling a circuit arrangement for the AC power supply of a plasma display panel in which the circuit arrangement comprises at least a transistor bridge constituted by the bridge transistors T1, T2, T3, T4, an input voltage U0, a capacitor Cp of the plasma cell and a charging circuit in the form of an auxiliary voltage Uh, a first auxiliary transistor T11 and a first coil L1, and in which said first auxiliary transistor T11 is rendered  
5 conductive at the beginning of the charging operation, characterized in that after the first auxiliary transistor T11 has been turned on, the second bridge transistor T2 of the half bridge continues to be turned on for a delay time  $t_v$  and is turned off after the delay time  $t_v$  has elapsed.
- 10 2. A method of controlling a circuit arrangement for the AC power supply of a plasma display panel, in which the circuit arrangement comprises at least a transistor bridge constituted by the bridge transistors T1, T2, T3, T4, an input voltage U0, a capacitor Cp of the plasma cell and a discharging circuit comprising an auxiliary voltage Uh, a second  
15 auxiliary transistor T12 and a second coil L2 and at the beginning of the discharging operation the second auxiliary transistor T12 is turned on, characterized in that after the second auxiliary transistor T12 has been turned on, the first bridge transistor T1 of the half bridge continues to be turned on for a delay time  $t_v$  and is turned off after the delay time  $t_v$  has elapsed.
- 20 3. A method as claimed in one of the claims 1 or 2, characterized in that the delay time  $t_v$  is about  $1/8$  of the oscillatory period.
4. A method as claimed in any one of the claims 1 to 3, characterized in that the  
25 input voltage U0 is generated by a DC voltage source.
5. A method as claimed in any one of the claims 1 or 4, characterized in that the auxiliary voltage Uh is applied to an auxiliary capacitor Cs.

6. A method as claimed in claim 5, characterized in that the capacitance of the auxiliary capacitor  $C_s$  exceeds by far the capacitance of the capacitor  $C_p$  of the plasma cell.

7. A plasma display panel comprising means for controlling a circuit  
5 arrangement for the AC power supply of the plasma display panel, the circuit arrangement comprising at least a transistor bridge constituted by the bridge transistors T1, T2, T3, T4, an input voltage  $U_0$ , a capacitor  $C_p$  of the plasma cell and a charging circuit comprising an auxiliary voltage  $U_h$ , a first auxiliary transistor T11 and a first coil L1 and is provided for turning on the first auxiliary transistor T11 at the beginning of the charging operation,  
10 characterized in that after the first auxiliary transistor T11 has been turned on, the second bridge transistor T2 of the half bridge continues to be turned on for a delay time  $t_v$  and is turned off after the delay time  $t_v$  has elapsed.